

ABSTRACT OF THE DISCLOSURE

A microcomputer comprises: a first electric power source VDD1 to which a condenser 30 used for backup is added; a second electric power source VDD2 to which the condenser 30 used for backup is not added; and a resistance load 40 added to the second electric power source. When a supply of voltage of the second electric power source VDD2 is stopped, a level of the second electric power source VDD2 is changed from a high level to a low level, and when the supply of voltage of the second electric power source VDD2 is resumed, the level of the second electric power source VDD2 is returned from the low level to the high level.